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Docket No.: 0925-0220PUS1
(PATENT)

THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Satoshi YAMANAKA et al.

Application No.: 10/541,611

Confirmation No.: 8340

Filed: July 7, 2005

Art Unit: N/A

For: PIXEL INTERPOLATION CIRCUIT, PIXEL
INTERPOLATION METHOD AND IMAGE
SCANNER

Examiner: Not Yet Assigned

LETTER SUBMITTING CORRECTED DRAWINGS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Attached hereto are 1 (one) sheets of corrected drawings.

Please substitute these corrected drawings for the corresponding 1 (one) sheets of drawings on file in the above-identified application.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fee required under 37 C.F.R. §§ 1.16 or 1.17; particularly, extension of time fees.

Dated: April 20, 2006

Respectfully submitted,

By 

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Attachments – one sheet of replacement drawings;
one sheet of annotated drawings.